

Fig. 1
(Prior Art)

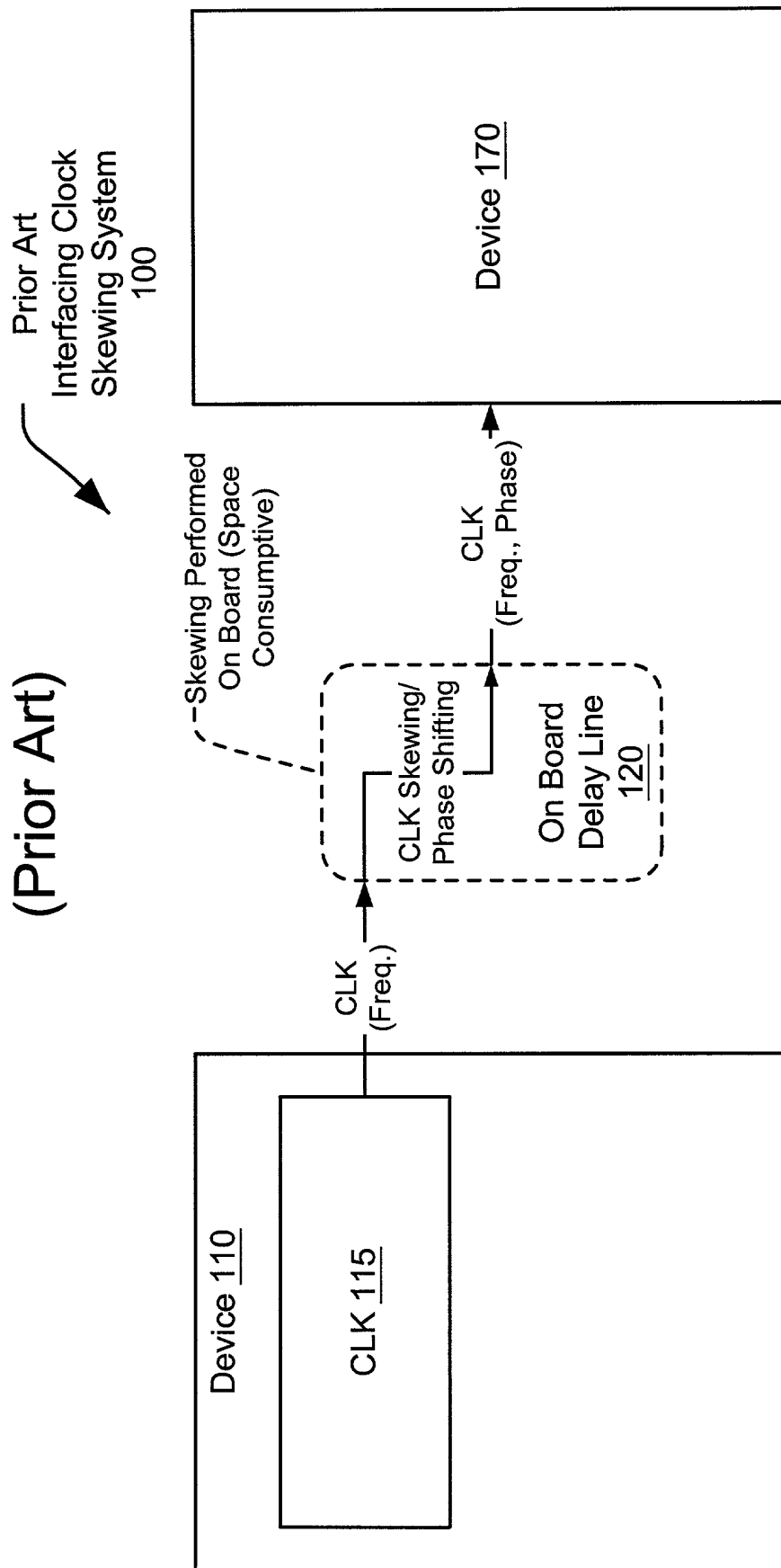
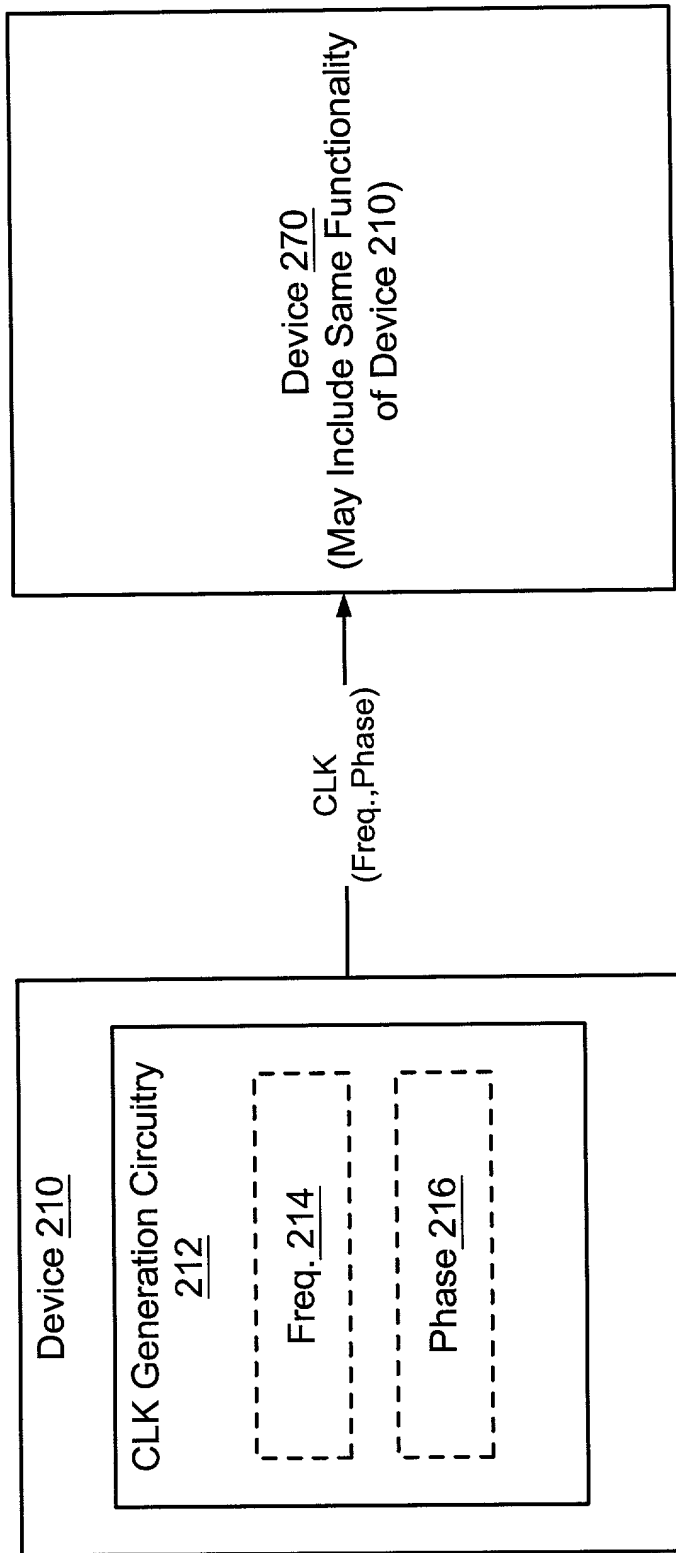


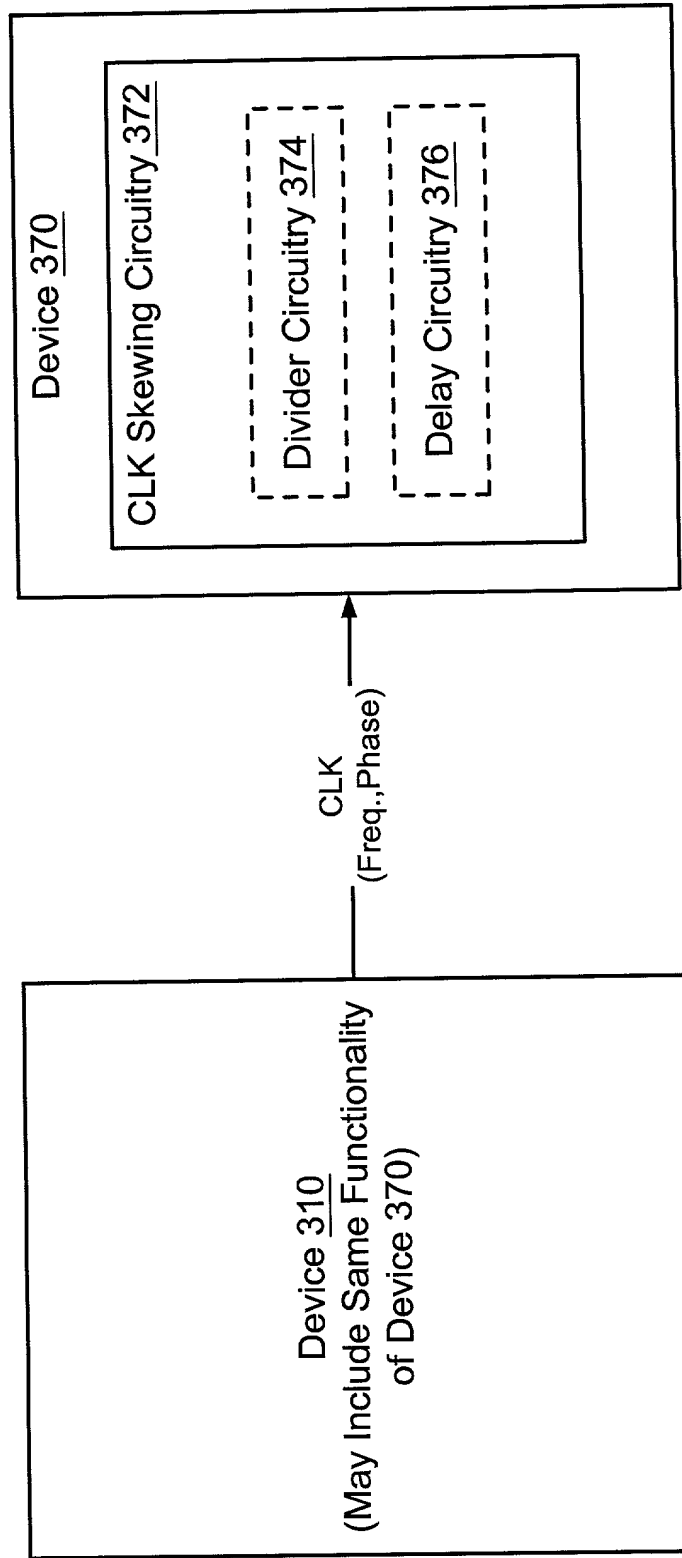
Fig. 2

Interfacing Clock
Skewing System 200



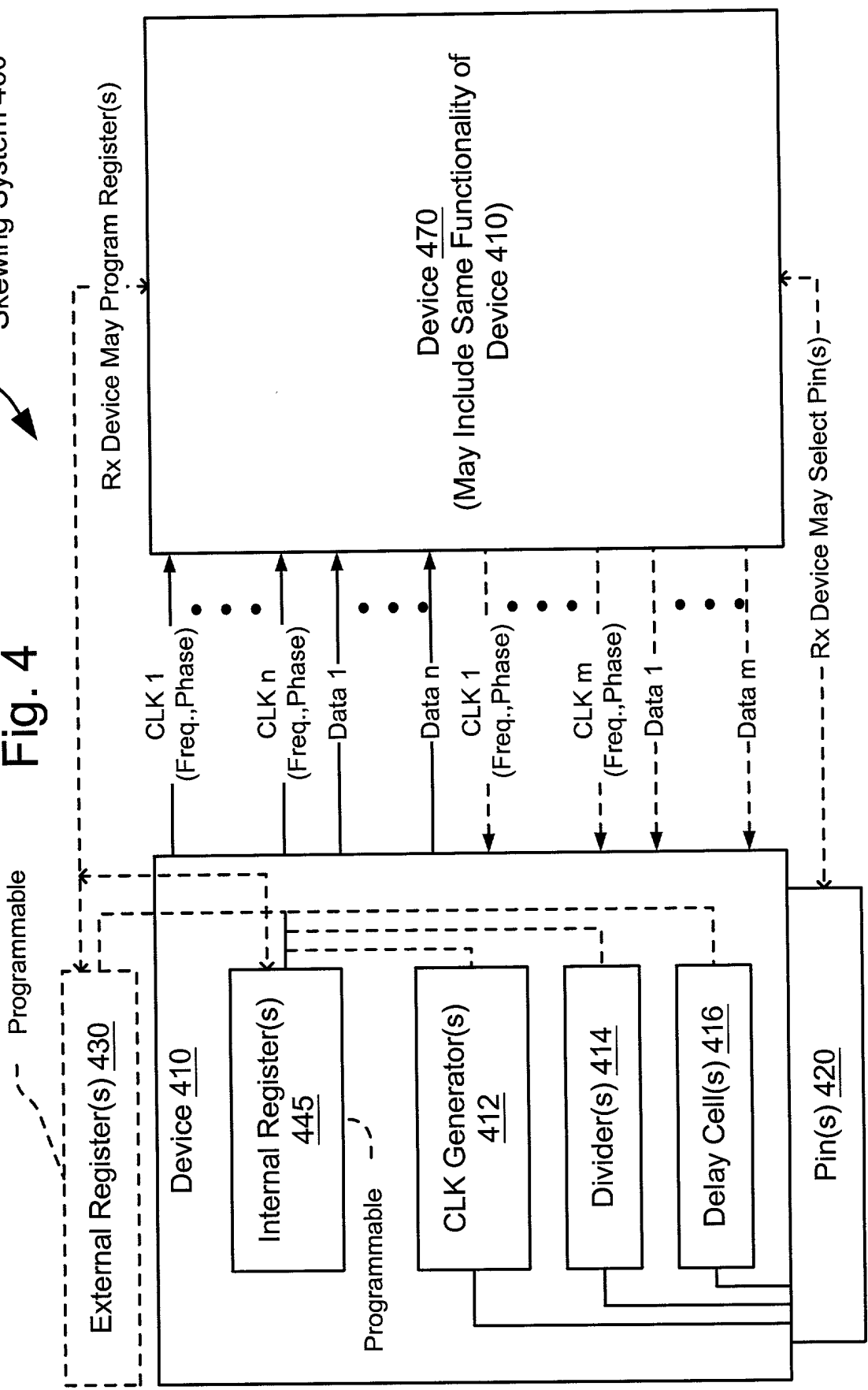
Interfacing Clock
Skewing System 300

Fig. 3



Interfacing Clock
Skewing System 400

Fig. 4



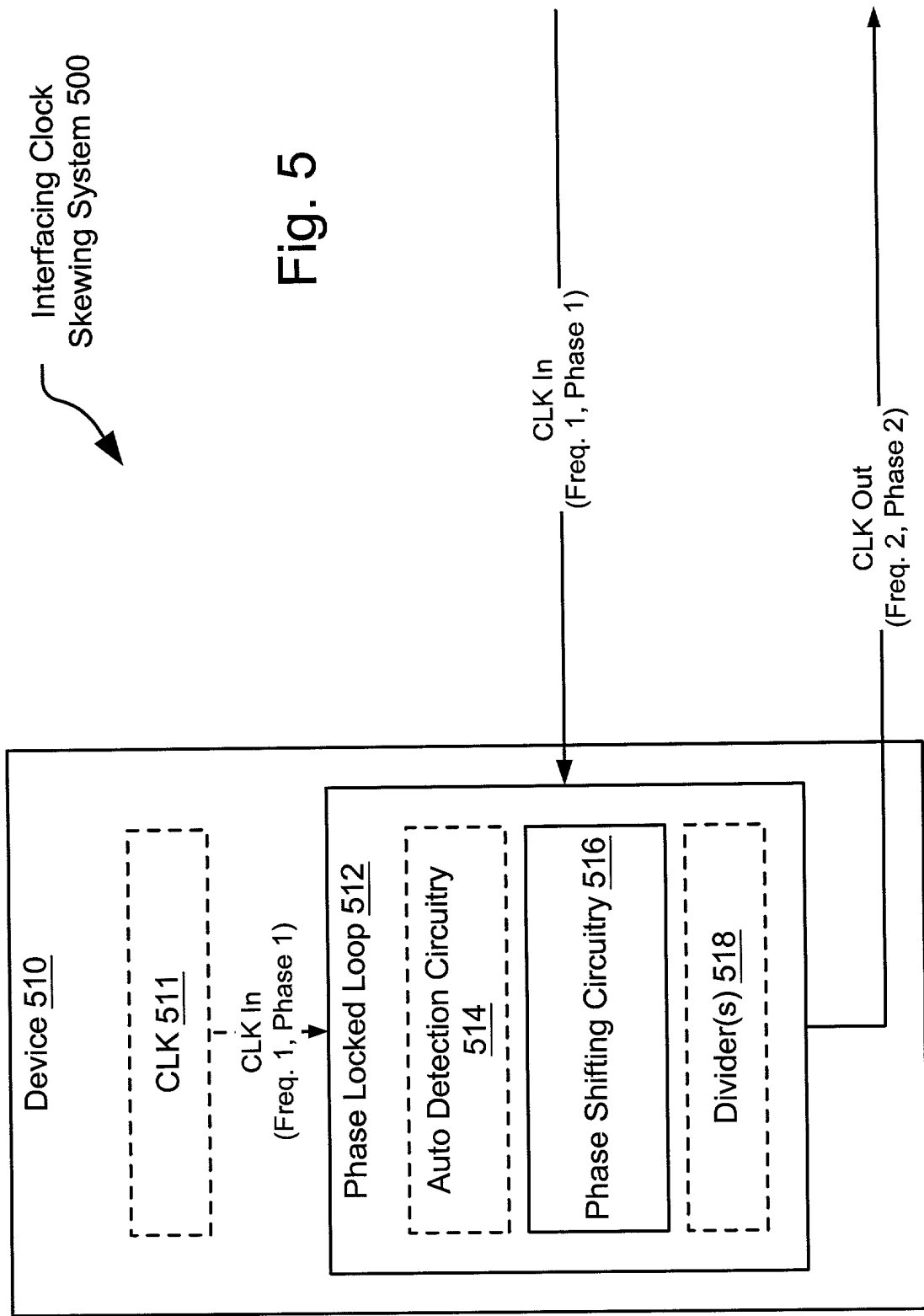


Fig. 5

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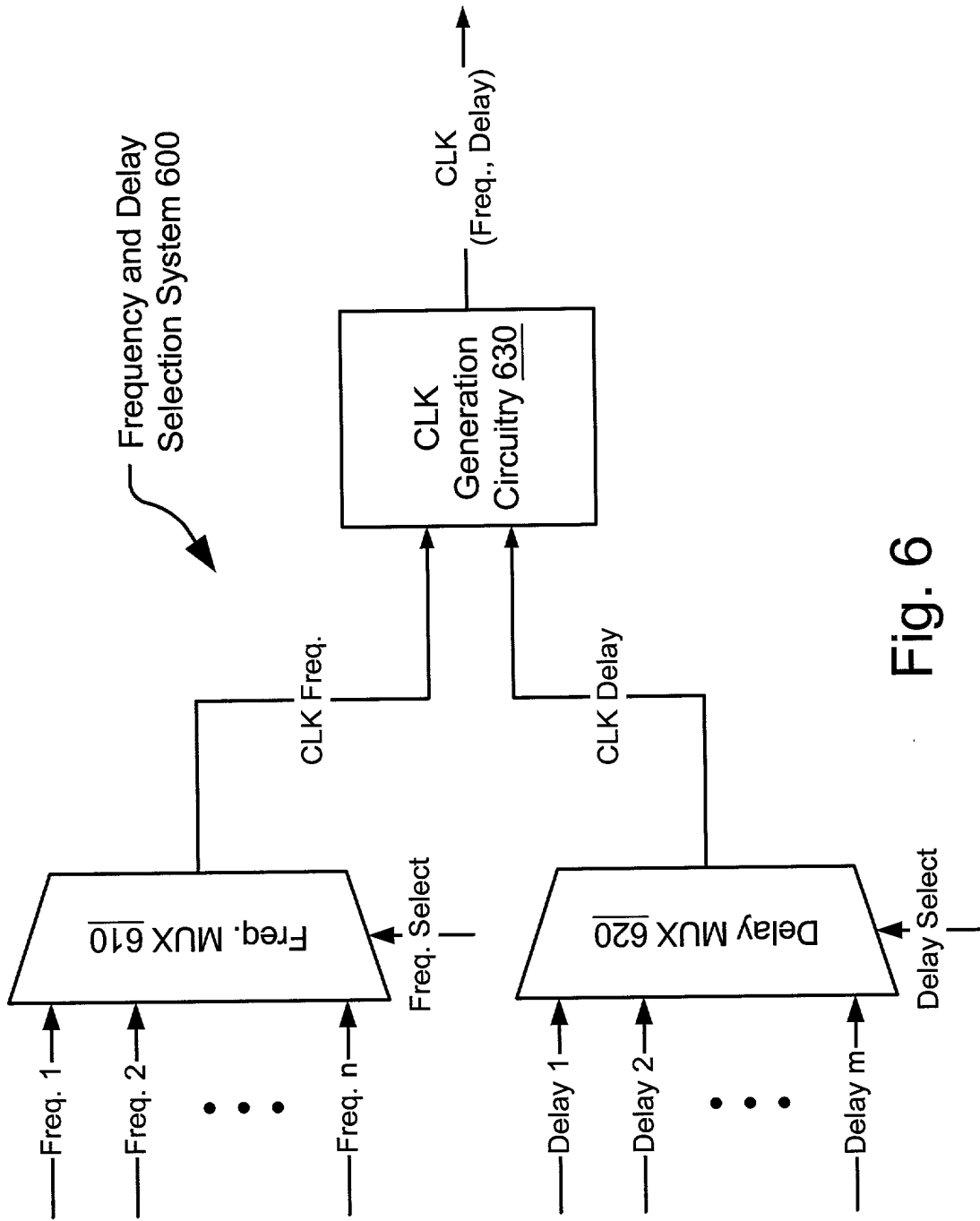


Fig. 6

Frequency and Delay
Selection System 700

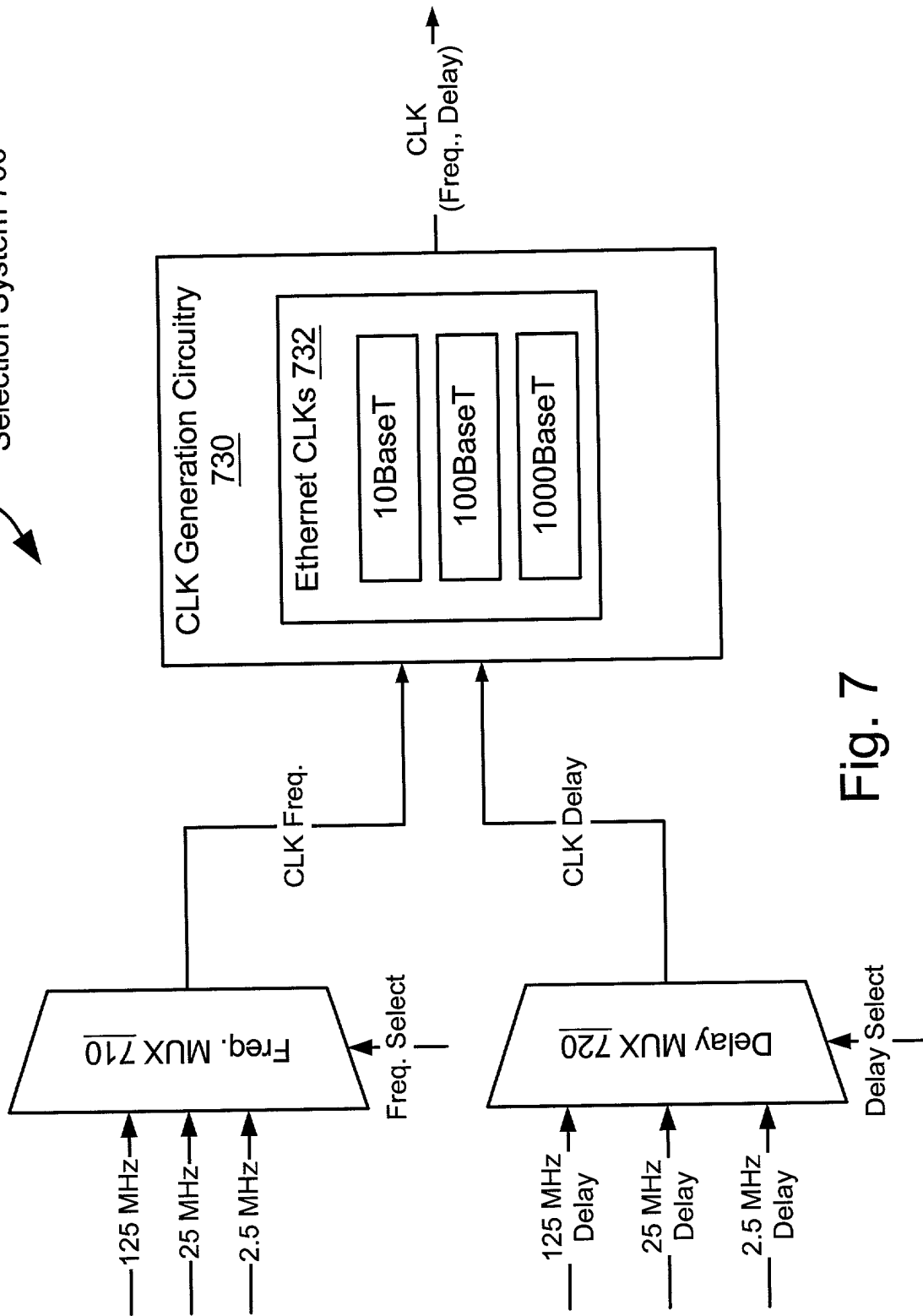


Fig. 7

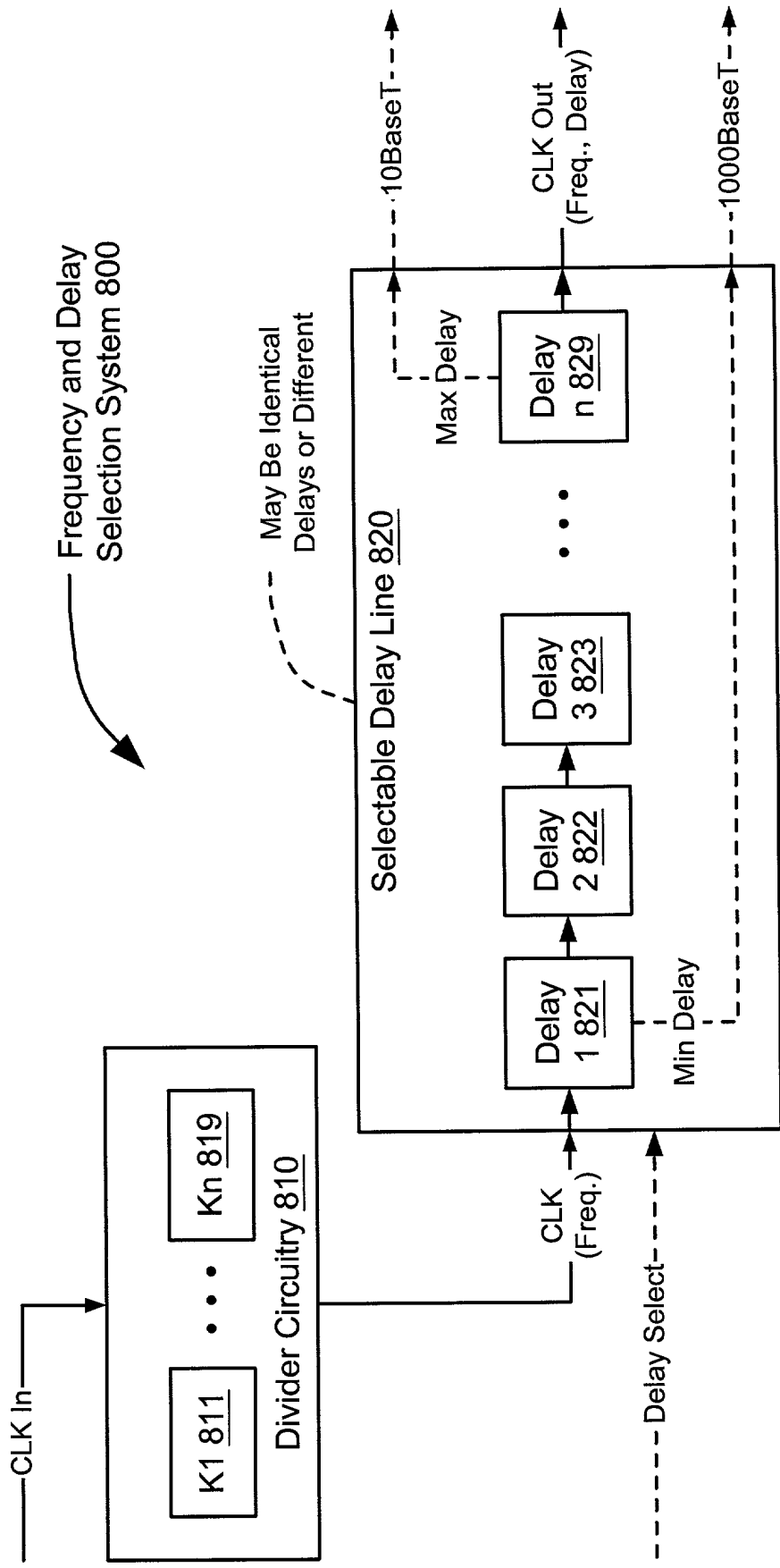


Fig. 8

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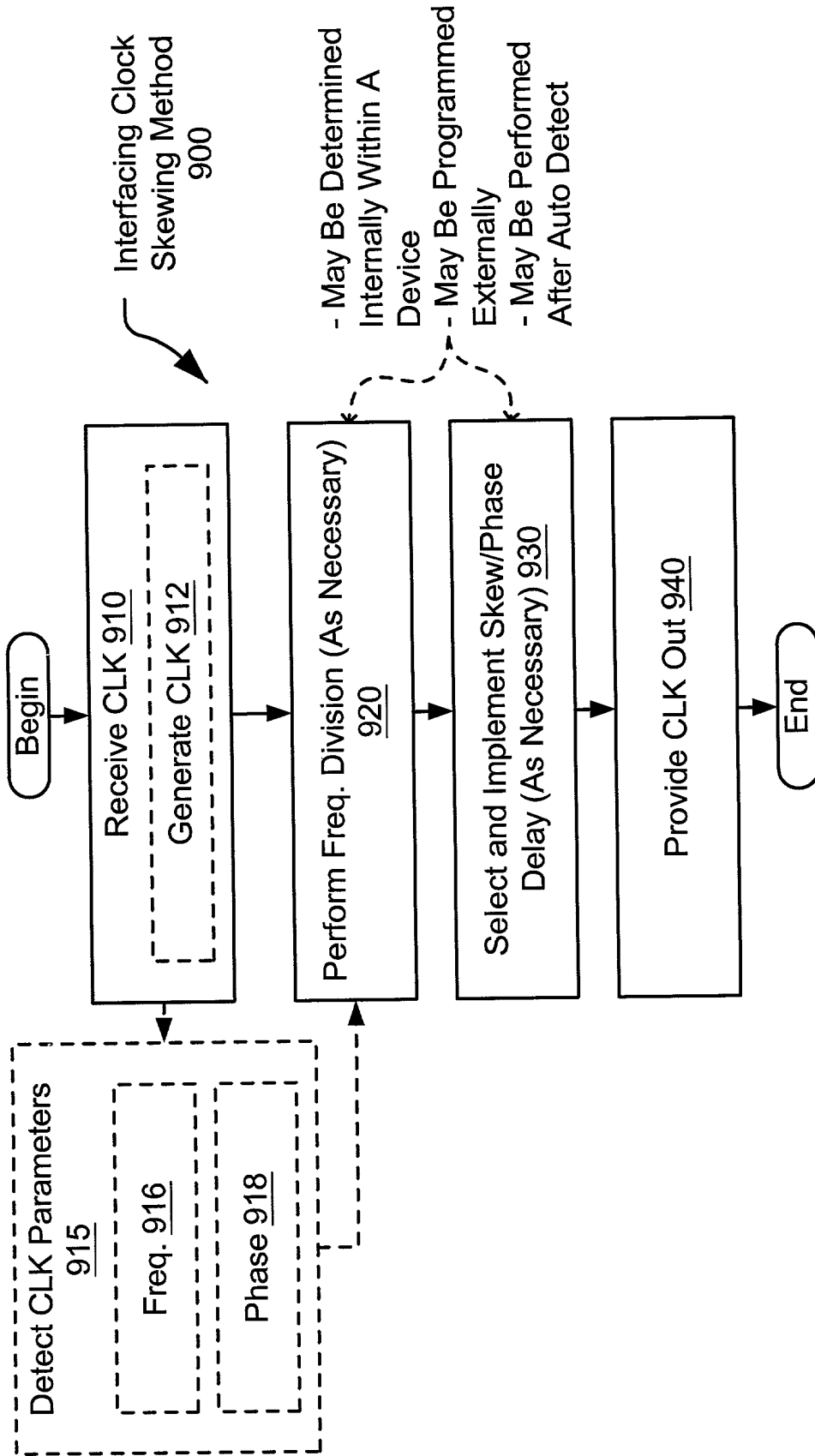


Fig. 9